

Electronic Acknowledgement Receipt

EFS ID:	1047771
Application Number:	10711532
Confirmation Number:	5531
Title of Invention:	Avoiding Excessive Cross-terminal Voltages of Low Voltage Transistors Due to Undesirable Supply-Sequencing in Environments With Higher Supply Voltages
First Named Inventor:	Bhupendra SHARMA
Customer Number:	23494
Filer:	Abdul Zindani/Gracia Sansom
Filer Authorized By:	Abdul Zindani
Attorney Docket Number:	TI-38242
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		T38242_111_OA.pdf	15070	yes	7

	Multipart Description		
	Doc Desc	Start	End
	Amendment - After Non-Final Rejection	1	1
	Amendment Copy Claims/Response to Suggested Claims	2	5
	Applicant Arguments/Remarks Made in an Amendment	6	7

Warnings:

Information:

Total Files Size (in bytes):	15070
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.